Notice of References Cited

Application/Control No. 10/044,731	Applicant(s)/l Reexamination PRAKASH, F	on
Examiner	Art Unit	
Jason Mitchell	2103	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	Α	US-6,189,141 B1	02-2001	Benitez et al.	717/153
*	В	US-6,327,704 B1	12-2001	Mattson et al.	717/153
*	С	US-6,343,375 B1	01-2002	Gupta et al.	717/152
*	D	US-6,363,522 B1	03-2002	Click et al.	717/160
*	Ε	US-6,487,716 B1	11-2002	Choi et al.	717/159
*	F	US-6,735,760 B1	05-2004	Dice, David	717/139
	G	US-	:		
	Н	US-			
	-	US-			
	J	US-			
	к	US-			
	L	US-			
	М	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N	1				
	0					
	Ρ					
	σ					
	R					
	S					
	Т					

NON-PATENT DOCUMENTS

		NON-FATERY BOOOMENTO
*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Schilling, J.L. "Optimizing Away C++ Exception Handling" ACM SIGPLAN Notices V.33(8), pp.40-47, 8/1998.
	V	Kawahito, M et al. "Effective Null Pointer Check Elimination Utilizing Hardware Trap", Proceedings of the ninth international conference on Architectural support for programming languages and operating systems, pp. 139-149, 1 1/2000
	w	Ogasawara, T. "A Study of Exception Handling and its Dynamic Optimization in Java", ACM SIGPLAN Notices, pp. 83-95, V36(1 1), 10/2001
	х	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.